

# ICCD 2004

## CALL FOR PAPERS

October 11 — 13, 2004  
Doubletree Hotel, San Jose, CA

*Sponsored by the IEEE Computer Society and  
IEEE Circuits and Systems Society,  
in cooperation with the IEEE Electron Devices Society*



## International Conference on Computer Design

### IMPORTANT DATES

Submission deadline : May 14th

Notification deadline : June 25th

Final manuscript : July 23rd

<http://www.iccd-conference.org>

The International Conference on Computer Design encompasses a wide range of topics in the research, design, and implementation of computer systems and their components. ICCD's multi-disciplinary emphasis provides an ideal environment for developers and researchers to discuss practical and theoretical work covering system and computer architecture, verification and test, design and technology, and tools and methodologies. **The theme for the 2004 ICCD conference is: The Convergence of Computing, Communications, and Consumer Electronics.** Submitted papers consistent with this theme are encouraged. Authors are invited to submit technical papers in accordance to the author's instructions describing original work in one of the following areas:

### Computer Systems Design and Applications

Advanced computer architecture for general and application-specific enhancement; Modeling and performance analysis; Support for operating systems and languages; Memory hierarchy; System design methods for uni- and parallel-processors.

### Processor Architecture

Microarchitecture design techniques: instruction-level parallelism, superscalar and dataflow techniques, pipelining, caching, branch prediction, multithreading, computer arithmetic; Embedded network, system-on-chip, and application specific processor design.

### Logic and Circuit Design

Circuit design for digital, memory, and mixed-signal systems; Design techniques for timing, power, noise, and layout optimization; Design prototyping; Signal processing and arithmetic circuits; Asynchronous circuits; Circuit optimization techniques for emerging process technologies.

### Tools and Methodology

Design methodologies for large systems; Concurrent engineering methodologies; Hardware description languages; high-level and logic synthesis, physical design, automatic analysis and optimization of timing, power, and noise; CAD algorithms and integrated CAD systems.

### Verification and Test

Simulation based and formal techniques for functional design verification; Equivalency checking, property checking, theorem proving; High-level design validation; Design error debug and diagnosis; Hardware/Software validation; Fault modeling; Fault simulation and ATPG; DFT and BIST; SoC testing.

### ICCD 2004 ORGANIZING COMMITTEE

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Papers describing novel methods and concepts or innovative features of new products, and focusing on the overall integration of these areas into the computer design process are of particular interest to ICCD. Papers may be accepted as either regular papers or as short papers. All papers will be presented during parallel technical sessions, and will be included in a published proceedings. At least one author of the paper must register for and attend the conference.

Proposals for embedded tutorials and panel discussions are also solicited, and should be sent to:

Pranav Ashar, [ashar@nec-labs.com](mailto:ashar@nec-labs.com)

#### Instructions to Authors

The submitted manuscript should closely reflect the final paper as it will appear in the Proceedings, which will be a maximum of eight pages in IEEE double column format. (Papers greater than six pages will incur an extra page charge.) The international review committee will give preference to papers that (excluding references, figures, and tables) do not exceed 3000 words to clearly present the work, methods, results, originality, significance, superiority, and applications of the techniques discussed.

#### Electronic Submission

Authors should plan on formatting their papers in Postscript format. Details on the electronic submission are available at :

<http://www.iccd-conference.org>.

Questions about the submission procedure may be sent to:

Ed Grochowski, [edward.grochowski@intel.com](mailto:edward.grochowski@intel.com)